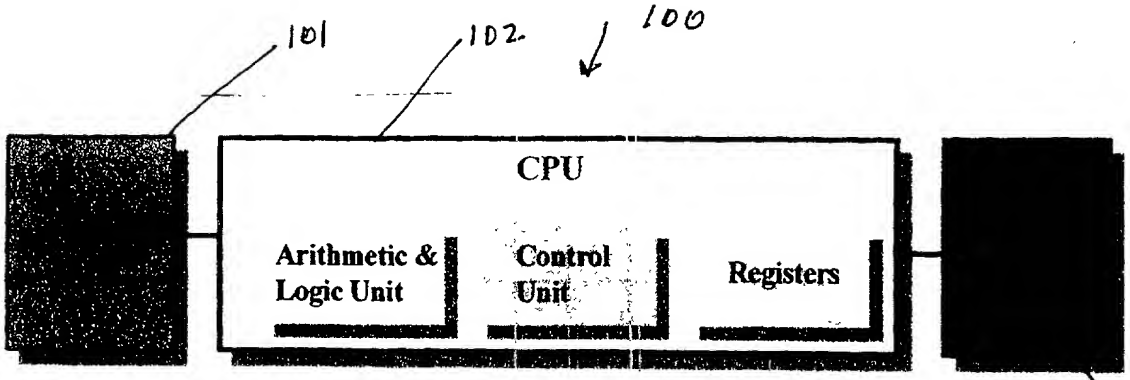

Figure 1. Block Diagram of a Typical Stored-Program Computer

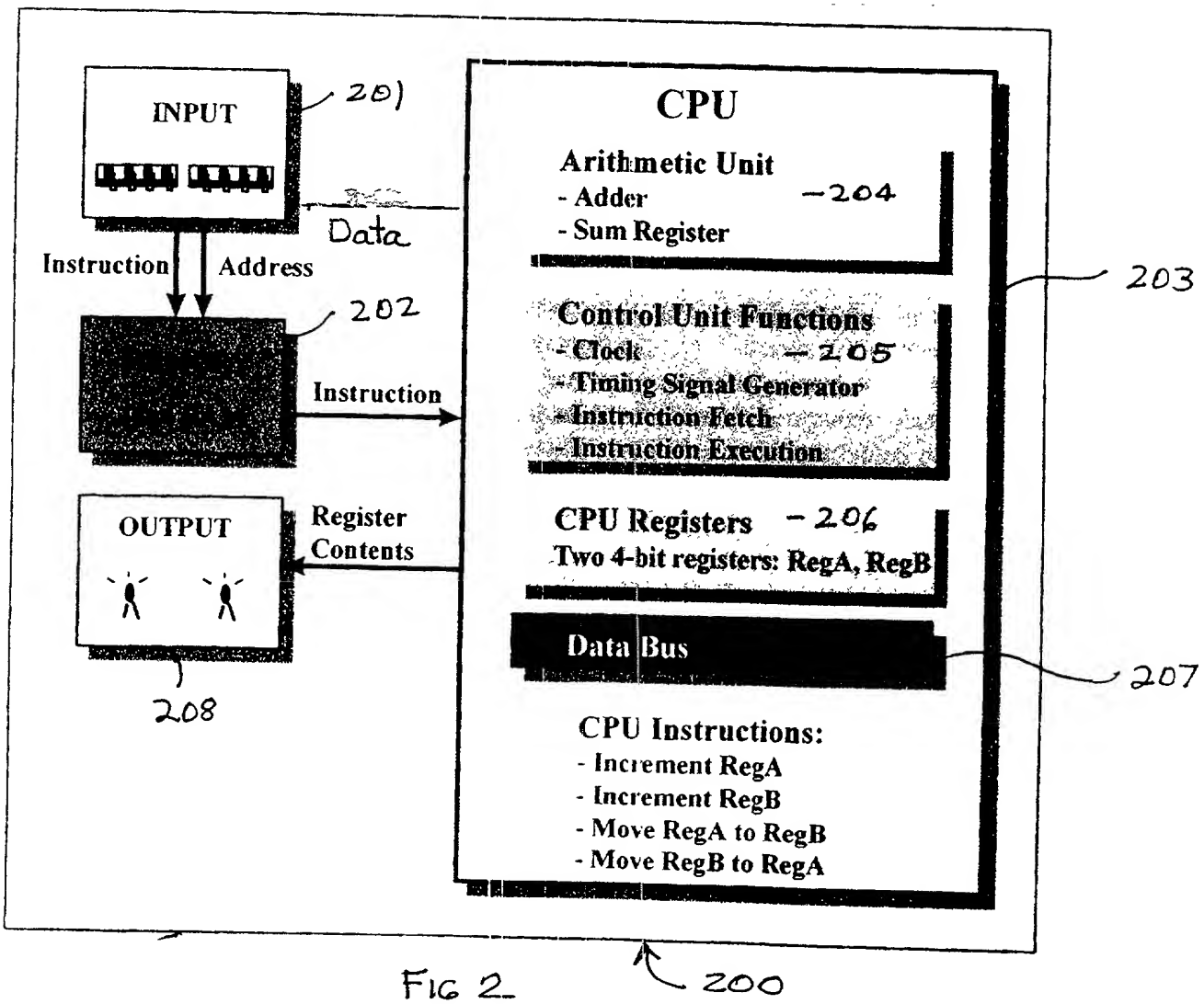

FIG 2

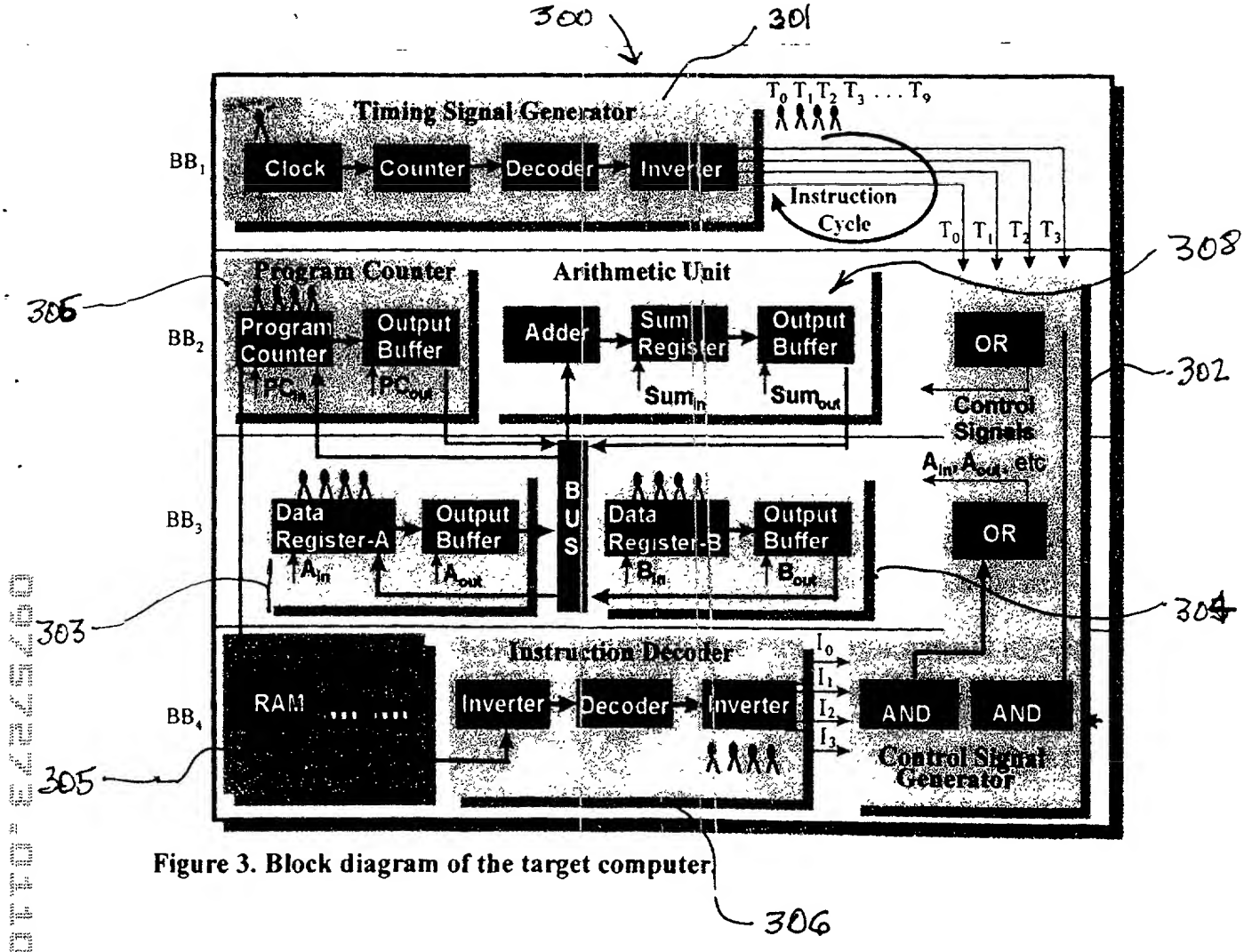

Figure 3. Block diagram of the target computer

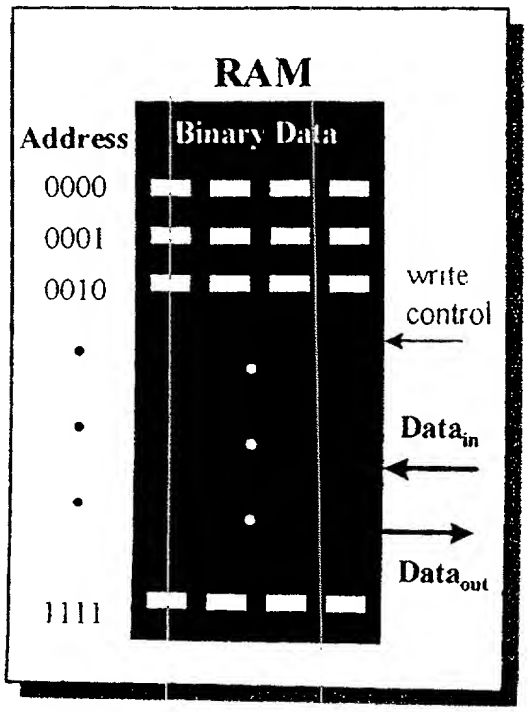

Fig 4